

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Verbauwheide et al.
Appl. No.	: 10/565,551
Filed	: September 11, 2006
For	: DYNAMIC AND DIFFERENTIAL CMOS LOGIC WITH SIGNAL- INDEPENDENT POWER CONSUMPTION TO WITHSTAND DIFFERENTIAL POWER ANALYSIS
Examiner	: Daniel D. Chang
Group Art Unit	: 2819

IN RE KATZ DECLARATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

1. This declaration is to establish that one of the co-authors of Tiri et al. ("A Dynamic and Differential CMOS Logic With Signal Independent Power Consumption to Withstand Differential Power Analysis on Smart Cards", ESSCIRC 2002, Proceedings of the 28th European Solid-State Circuit Conference, September 24-26, 2002, Florence, Italy, 2002), who was not named as co-inventors of the present application, did not contribute to the conception of the claimed invention.

2. The person making this declaration, Ingrid M. Verbauwheide, is the joint inventor of the present application.

3. The conceptualization of the present invention was made jointly by me and the other named inventor Kris J. V. Tiri. We determined that a dynamic and differential CMOS logic with signal independent power consumption could withstand differential power analysis.


4. The third author (Moonmoon Akmal) of the paper mentioned in paragraph 1 of this declaration, did not provide an inventive contribution to the pending claims. Moonmoon Akmal

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was a student who collected the simulation results for the reference module implemented in static CMOS. Moonmoon Akmal was not involved with the development of the dynamic and differential logic, and she was not involved in the simulations of the module implemented in the dynamic and differential CMOS logic.

5. I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful, false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or patent issuing therefrom.

Dated: Dec 19, 2007

By: 
Ingrid M. Verbaauwhede

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